



Docket No.: GR 98 P 2124 P

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By:

Date: July 16, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Josef-Georg Bauer et al.
Applic. No. : 09/761,240
Filed : January 17, 2001
Title : Power Semiconductor Element with an Emitter Region and a Stop Zone in Front of the Emitter Region
Examiner : Johannes P. Mondt - Art Unit: 2826

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

German Published, Non-Prosecuted Patent Application DE 39 17 769 A1 (Gerstenmaier et al.), dated December 6, 1990, thyristor with emitter shunt connections;

European Patent Application EP 0 760 528 A2 (Stephani et al.), dated March 5, 1997, semiconductor device on silicon base with a high blocking voltage edge termination;

International Search Report dated January 17, 2000.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

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patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,


For Applicants

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Date: July 16, 2002

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